

ABSTRACT OF THE DISCLOSURE

A plasma display panel includes a front substrate and a rear substrate opposing one another; display electrodes formed on the front substrate, a dielectric layer formed on the front substrate covering the display electrodes, barrier ribs formed on the rear substrate and including first barrier rib members formed in a direction orthogonal to the display electrodes, and second barrier rib members formed in a direction parallel to the display electrodes, the first barrier rib members intersecting the second barrier rib members to define discharge cells; phosphor layers formed in the discharge cells, and address electrodes realized through conductive wires and coated with a dielectric material, the address electrodes being formed orthogonal to the display electrodes in the discharge cells. The address electrodes may be mounted on the second barrier rib members orthogonal to the display electrodes in the discharge cells.